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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/679,794	10/06/2003	Michael L. Babala	1-24754	3701		
4859	7590 10/28/2004		EXAM	EXAMINER		
	AN SOBANSKI & TO	CHAPMAN JR, JOHN E				
ONE MARIT	TIME PLAZA FOURTH STREET	FLOOR	ART UNIT	PAPER NUMBER		
TOLEDO, C			2856			

DATE MAILED: 10/28/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
		10/679,794	BABALA, MICHAEL	L L.		
Office Action Summary		Examiner	Art Unit			
		John E Chapman	2856			
The MAILING DATE of this Period for Reply	communication appe	ars on the cover sheet wit	h the correspondence add	Iress		
A SHORTENED STATUTORY P THE MAILING DATE OF THIS C - Extensions of time may be available under the after SIX (6) MONTHS from the mailing date - If the period for reply specified above, the saliure to reply within the set or extended period for reply within the set or extended period patent term adjustment. See 37 CFF	OMMUNICATION. e provisions of 37 CFR 1.136 of this communication. than thirty (30) days, a reply winaximum statutory period will riod for reply will, by statute, of the months after the mailing of	(a). In no event, however, may a re vithin the statutory minimum of thirty apply and will expire SIX (6) MON ause the application to become AB	oply be timely filed (30) days will be considered timely. FHS from the mailing date of this cor ANDONED (35 U.S.C. § 133).	mmunication.		
Status		•				
1) Responsive to communicat	ion(s) filed on 24 Se	otember 2004.				
2a) This action is FINAL .	_ ·					
	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims						
4) ⊠ Claim(s) <u>11-17 and 20-24</u> i 4a) Of the above claim(s) _ 5) □ Claim(s) is/are allow 6) ⊠ Claim(s) <u>11-14,20,23 and 2</u> 7) ⊠ Claim(s) <u>15-17,21 and 22</u> i 8) □ Claim(s) are subject	is/are withdraw ed. <u>4</u> is/are rejected. s/are objected to.	n from consideration.				
Application Papers						
9) The specification is objected10) The drawing(s) filed onApplicant may not request that	is/are: a)☐ acce t any objection to the d	pted or b)⊡ objected to l rawing(s) be held in abeyan	ce. See 37 CFR 1.85(a).			
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
Priority under 35 U.S.C. § 119						
•	one of: e priority documents e priority documents d copies of the priori International Bureau	have been received. have been received in A ty documents have been (PCT Rule 17.2(a)).	pplication No received in this National S	Stage		
Attachment(s)			(070.440)			
Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawin Information Disclosure Statement(s) (P Paper No(s)/Mail Date		Paper No(s	summary (PTO-413) s)/Mail Date nformal Patent Application (PTO)-152)		

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DETAILED ACTION

1. A request for continued examination under 37 CFR 1.114, including the fee set forth in 37 CFR 1.17(e), was filed in this application after allowance or after an Office action under *Ex Parte Quayle*, 25 USPQ 74, 453 O.G. 213 (Comm'r Pat. 1935). Since this application is eligible for continued examination under 37 CFR 1.114, and the fee set forth in 37 CFR 1.17(e) has been timely paid, prosecution in this application has been reopened pursuant to 37 CFR 1.114. Applicant's submission filed on September 24, 2004 has been entered.

- 2. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.
- 3. Claims 14 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Condne et al. in view of Greiff et al.

Condne discloses an inertial sensor comprising a sensor element 20 mounted on a base member 16, and a plurality of signal conditioning circuits 36 and 40 connected to the sense element. High frequency and low frequency accelerations comprise different ranges. Hence, the only difference between the claimed invention and the prior art consists in mounting the sense element on a silicon wafer. Greiff teaches that it is known in the art to mount an accelerometer on a silicon wafer in order to provide a micromechanical accelerometer. Accordingly, merely to mount the sense element of Condne on a silicon wafer would have been obvious to one having ordinary skill in the art in order to provide a micromechanical accelerometer.

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4. Claims 11-14, 23 and 24 are rejected under 35 U.S.C. 103(a) as being unpatentable over Broillet et al. in view of Greiff et al.

Broillet discloses an inertial sensor comprising an acceleration sensor 31 mounted on a body, which is subjected to vibrations, and a plurality of signal conditioning circuits 33 and 34 connected to the accelerometer. Low frequency vibrations comprise a first range of change in a body motion parameter, and high frequency vibrations comprise a second range of change in a body motion parameter. Hence, the only difference between the claimed invention and the prior art consists in mounting the sense element on a silicon wafer. Greiff teaches that it is known in the art to mount an accelerometer on a silicon wafer in order to provide a micromechanical accelerometer. Accordingly, merely to mount the acceleration sensor of Broillet on a silicon wafer would have been obvious to one having ordinary skill in the art in order to provide a micromechanical accelerometer.

Regarding claims 11 and 13, it is well known in the art to provide signal conditioning circuitry integral with a silicon wafer in order to provide a compact sensor, as well as separately therefrom in order to provide interchangeability of different signal conditioning circuitry with a single sensor.

5. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Broillet in view of Greiff as applied to claim 12 above, and further in view of Flash et al.

Flash teaches that it is known in the art to form an application specific integrated circuit 14 on a silicon wafer 10. Accordingly, it would have been obvious to one having ordinary skill

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in the art to include the signal conditioning circuits integral within an application specific integrated circuit formed on a silicon wafer.

6. Claims 15-17, 21 and 22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the

base claim and any intervening claims.

7. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to John E Chapman whose telephone number is (571) 272-2191. If

attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hezron

Williams can be reached on (571) 272-2208. The fax phone number for the organization where

this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR

system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

John E Chapman Primary Examiner

Art|Unit 2856